

#### **Contact:**

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#### Financial:

Company stage: pre-seed Previous capital: \$100K owner Founder ownership: 100% Monthly burn: (\$3K) Capital Seeking: \$10M seed

#### **Use of Funds:**

70% Al processor development 15% marketing and sales 5% operations 5% legal 5% other

#### **Advisors**

(seeking)

## **Financials and Projections**

available upon request.

## **Traction**

Ten leapfrog hardware patents granted, four patents in core AI (class G06N3), 1 in forward error correction of arithmetic (class H03M). Mod-9 and TPU matrix multiplier technologies proven; initial releases planned. Error correcting IP proven and prototype tested. AI processor under design. Professional papers, whitepapers published. Managed web site.

#### Pitch

MaiTRIX develops advanced computer technology to replace conventional processing techniques for heavy workload AI processing and high-speed matrix arithmetic. MaiTRIX IP provides significant increases to computational speed and accuracy and provides significant decreases in power consumption. MaiTRIX owns the world's first true, continuous error detection and correction of hardware accelerated matrix arithmetic which allows over-clocking AI processors by up to 50%.

### **Problem**

Advances in cloud-based AI have pushed conventional CPU, GPU, and FPGA capabilities to their limit. Quantum based computer capability is oversold and is decades away from true arithmetic operation. Moore's law has halted due to fundamental barriers in material science, pushing semiconductor manufacturers to scramble and panic. The problem is there is no "break-out" alternatives for increasing computational efficiency in a significant way.

### Solution

Now there is a breakthrough technology that provides a new dimension of processing called *modular computation*. Modular computation is not an alternative material science or new type of IC design; instead, it's a new choice of arithmetic used to perform computation itself. The new arithmetic is highly efficient for product summations, which is at the heart of all operations used in modern artificial intelligence. For specific key AI applications, modular computation will provide at least 10 to 50 times performance increase over conventional binary arithmetic.

# **Business model**

MaiTRIX currently offers its TPU matrix multiplier, and advanced error correcting technologies as licensed IP cores. MaiTRIX is developing an advanced AI processor core using its' modular computation. MaiTRIX IP cores target AI acceleration, cloud computing, cryptography, financial, pharmaceutical, defense, and space-based machine intelligence. MaiTRIX IP cores are implemented using available FPGA/ASIC technologies.

#### **Markets**

World-wide server markets, high-end semiconductor OEMs like Intel and Nvidia, and cloud-based giants like Google, AWS, Microsoft, and IBM.

## **Trends**

In 2021, the top four U.S. technology companies spent over \$100 billion in R&D seeking an edge in Artificial Intelligence. Over \$3 billion is invested in Quantum computing, which will not impact the cloud-based AI market anytime soon. It is widely accepted the power demands of future cloud-based AI cannot be supported using existing technology. As the number of AI processors increase, so do errors. Modular computation offers significantly higher performance, consumes significantly lower power, and provides error corrected results. Modular computation will co-exist and enhance other digital and quantum technologies of tomorrow.